Digital Correlator and Phased Array Architectures for Upgrading ALMA

Alain Baudry & Benjamin Quertier Université de Bordeaux / LAB on behalf of

ngCorrelator Study Team

ALMA Developers' Workshop, May 25th-27th, 2016

Digital Correlator and Phased array Architectures for Upgrading ALMA



Digital correlator and phased array architectures for upgrading ALMA

One year development study on ngALMA Correlator

- Study start date: April 2016; Kick-off: 10-11 May, 2016
- Final report: end of May 2017

Core team

J. Weintroub (PI, SAO/Harvard), A. Baudry (U. Bx/LAB), B. Carlson (CNRC), S. Doeleman (Harvard), R. Escoffier(NRAO), R. Lacasse (NRAO), M. Rupen (CNRC), A. Saez (JAO)

Attending part or all of Kick-off meeting

SAO: L. Blackburn, R. Blundell, L. Greenhill, J. Moran, R. Primiani, E. Tong, R. Wilson, A. Young

Berkeley: J. Hickish

Haystack: G. Crew, M. Hecht, L. Matthews

NRAO/Socorro: S. Ashton, C. Langley

Objectives

A conceptual design for a new generation ALMA correlator

Proposed specifications:

- 16GHz/sideband per polarization
- Natively integrated features (4 bits, phased array)
- Very long baselines (eg 300 km)
- Additional antennas, etc
- Multibeam (10 to 100? could be traded against BW)

Prepare final report for May 2017:

- Validate assumptions and design flows
- Correlator location question (AOS vs OSF)
- A conceptual design with sufficient detail to . . .
- . . . propose for follow-on development project to detail design

Kick off meeting agenda

ALMA Correlator/Phased Array Development Study: kickoff meeting agenda v10

Meeting dates 10, 11 May, 160 Concord Avenue, Room M340

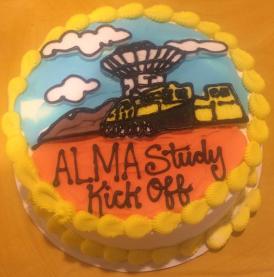
	Speaker or facilitator	Comments	
I			
ffee, light snacks			
elcome	Charles Alcock		
cience drivers			
	Ray Blundell		
eting plan and logistics	Jonathan Weintroub		
ence drivers for ngALMA	Michael Rupen		
MA Bandwidth Limits in			
year 2025	Rich Lacasse		
EHT and ALMA	Shep Doeleman		
e ASAC documents:			
thways to Developing			
		ASAC: ALMA Scientific	
veloping ALMA"	Alain Baudry	Advisory Committee	
fee break and discussion			
rrelators			
'ARM	Rurik Primiani		
	Michael Rupen		
,			
nch	List of quick food shops in wa	alking distance provided	
e HERA correlator	Jack Hickish		
ong and weak points of			
current ALMA correlator	Ray Escoffier		
e ALMA Correlator			
Session C: Recent developments in VLBI Phased Arrays			
e ALMA Phasing Project	Shep Doeleman		
P phased array algorithms	Geoff Crew		
,	André Young		
cussion over coffee	all		
pics			
ers to OSF: location of the			
kt ALMA digital back end	Shep Doeleman		
cussion: correlator modes	Facilitated by Michael		
d feature creep	Rupen		
ctrical power for ALMA:			
· ·	Facilitated by Rich Lacasse		
· ·			
cussion, integration of			
•	Facilitated by Brent Carlson		
	Icome Identice drivers Invited as a test bed for ALMA. Invited as a test bed for Invited as a test	Icome Identification	

Wednesday 11 May 201	16	
8:30am to 9:00am	Coffee, light snacks	
9:00-9:05am	Review and adjustments	Jonathan Weintroub
Session E: Next general	tion hardware	
9:05 to 9:25am	PowerMX framework	Brent Carlson
9:25 to 9:35	CASPER Framework	Jonathan Weintroub
9:35 to 9:45am	LEDA correlator	Lincoln Greenhill
	Fast Digitization and Digital	i i
	Transmission for Upgraded	i i
9:45 to 10:05am	ALMA Bandwidths	Alain Baudry
	Multi-core ADC artifacts and	
10:05 to 10:20am	calibration	Bob Wilson
10:20 to 10:30am	ADC clock offset scheme	Brent Carlson
10:30 to 10:40	SMA Receiver Upgrade	Edward Tong
10:40 to 10:50am	discussion	i i
10:50 to 11:10am	coffee	all
Session F: Software and		
	Discussion: Interaction with	i i
11:10am to 11:30am	Joint ALMA Observatory	Facilitated by Mike Hecht
	ALMA software environment	·
11:30am to 11:45am	considerations	Alejandro Saez
	The importance of diagnostic	ļ
	tools: lessons from the	!
	current correlator	i _{ster} j
11:45am to 12:00 noon	deployment	Alejandro Saez
12:00 to 1:00pm	lunch	List of quick food shops in walking distance provided
4.00	Meet in M340, walk to 60	A.II
1:00pm to 1:30pm	Garden for Group photo	All
4.20 +- 4.20	(discussion duiring photo	
1:20pm to 1:30pm	Isession) project planning, and wrap up	all
session G: Integration,	Discussion: design	
1:30 to 2:00pm	methodology in proposal	Facilitated by Rurik Primiani
1.30 to 2.00pm	Round table discussion: A	racincated by Nurik Frimiani
	vision of the next ALMA	Facilitated by Lindy
2:00 to 2:00pm	correlator, and specifications	
2:00 to 3:00pm	1	
3:00 to 3:15pm	C'ville meeting feedback	Facilitated by Rich Lacasse
3:15-3:30pm	coffee	all
		Chris Langley and Sylas
3:30 to 3:40pm	Soccorro ADC Study	Ashton
	Study planning, work	Facilitated by Jonathan
3:40 to 5:00pm	assignments, deadlines	Weintroub

Selected examples of work packages

- Draft first scientific specifications with reference to ASAC
- Iteration of a design methodology memo
- Explore DSP platforms (ASIC vs FPGA vs GPU vs CPU)
- Large channelizer analysis for FX architectures
- F to X cross-connect methodology
- X-engine design project
- Study of cooling methods
- Scenarios for proof of concept follow-on project







- Next Generation ALMA correlator study is NOT the ALMA Enhancement study led by NRAO (PI, R. Lacasse)
 - Enhancement study: another 1 year study; concerns ALMA Baseline Correlator
- Enhancement study Objectives
 - x2 processed BW & x8 spectral resolution
 - Decrease the number of cards (4 times less correlator cards) and improve power dissipation
 - Use current infrastructure, minimum disruption
 - Evaluate costs
 - Main cost driver: the new correlator chip
 - New Digitizer (X2 BW), New DRx & TFB cards, New Correlator cards

Enhanced Correlator is interim, prior to the ngALMA Correlator